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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet 1 of 1

**Complete if Known**

Application Number	10/660,986
Filing Date	September 12, 2003
First Named Inventor	Louis K. Scheffer
Art Unit	Not yet assigned 2825
Examiner Name	Not yet assigned VUTHE
Attorney Docket Number	CA7016672001

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
VS	1	"International Technology Roadmap for Semiconductors", 2001	
	2	McINERNEY, RORY et al., "Methodology for Repeater Insertion Management in the RTL, Layout, Floorplan and Fullchip Timing Databases of Itanium Microprocessor", International Symposium on Physical Design, 2000, pp. 99-104.	
	3	CHINNERY, D.G. et al., "Closing the Gap between ASIC and Custom: An ASIC Perspective", Proceedings of the Design Automation Conference, June 2000, pp. 637-342.	
	4	CARLONI, LUCA P. et al., "Theory of Latency-Insensitive Design", IEEE Transactions in Computer-Aided Design of Integrated Circuits and Systems, Vol. 20, No. 9, September 2001, pp. 1059-1076.	
	5	CARLONI, LUCA P. et al., "Latency Insensitive Protocols", Computer Aided Verification, 11 <sup>th</sup> International Conference, CAV'99, Trento, Italy, July 1999, pp123-133.	
	6	"CARLONI, LUCA P. et al., "A Methodology for Correct-by-Construction Latency Insensitive Design", Proc. ICCAD, San, Jose, CA, November 1999, pp. 309-315.	
VS	7	LU, RUIBING et al., "Flip-Flop and Repeater Insertion for Early Interconnect Planning", Proceedings of the 2002 Design, Automation and Test in Europe Conference, 2002.	

Examiner Signature	VUTHE SIEK	Date Considered	4/6/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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<b>Examiner Name</b>	<b>Vuthe Siek</b>
<b>Attorney Docket Number</b>	<b>CA7016672001</b>

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**Jul 19 2004**

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## NON PATENT LITERATURE DOCUMENTS

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**Examiner's  
Signature**

VUTHE SIEK

Date Considered 4/6/05

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